

## ABSTRACT

5 An inventive electronic circuit includes central processing  
means having a clock connection and a data connection, as  
well as a peripheral unit having a clock connection and a  
data connection, the clock connection of the peripheral  
unit being connected to a signal output of a controllable  
oscillator or to an external clock input. Synchronization  
10 means having a first and a second data connection is  
connected, the first data connection being connected to the  
data connection of the peripheral unit. In addition a data  
bus connects the data connection of the CPU and the second  
data connection of the synchronization means. The clocking  
15 of the peripheral unit asynchronous to the central  
processing unit yields a more effective operation being  
better adjustable to certain parameters, such as, for  
example, the application and the energy of the electronic  
circuit available.

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Figure 1